

Gain-Enhanced LTCC System-on-Package for Automotive UMRR Applications

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Abstract—A novel Low Temperature Co-fired Ceramic (LTCC) based SoP for automotive radar applications is presented. For the first time a combination of a relatively low dielectric constant LTCC substrate and a high dielectric constant LTCC superstrate has been incorporated to enhance the overall gain of the module. The superstrate can provide additional protection to the integrated circuits (IC) in the harsh automotive environment. A custom cavity in the LTCC substrate can accommodate the IC, which feeds an aperture coupled patch antenna array. The cavity is embedded below the ground plane that acts as a shield for the IC from antenna radiation. It is estimated that with mere 10 dBm of transmitted RF power the miniature SoP module (sized 2.0 cm x 2.0 cm x 0.22 cm) can communicate up to 67 m. The design's compactness, robustness, transmission power and resultant communication range are highly suitable for Universal Medium Range Radar (UMRR) applications.

Index Terms—Low Temperature Co-fired Ceramic (LTCC), System on Package (SoP), Universal Medium Short Range Radar (UMRR)

I. INTRODUCTION

Ever since the introduction of the first short range radar (SRR) system in the 2005 Mercedes S class model, interest in the automotive radar applications at the unlicensed 24 GHz (Industrial, Scientific and Medical) band has been gathering a great deal of momentum with an increasing number of applications being envisaged [1], [2]. UMRR is one such example, which operates in the 24 GHz band, and is generally utilized for advanced automotive driver assistance applications such as high precision parking aid, blind spot detection and high sensitivity pre-crash detection [3]. In addition, UMRR can be employed as a support for cut-in, stop and go situations in the high range Adaptive Cruise Control (ACC) systems [4]. However, the above mentioned radar applications are still limited to very expensive vehicles due to their high cost implementations [5].

A suitable low cost solution for such automotive radars is the System-on-Package (SoP) platform that can remove the barrier against a speedy introduction of such systems into the lower class, high-volume car market. SoP integrates multiple functions into a single, compact, low cost and high

performance packaged module. It reduces the system size and cost immensely by transforming millimeter-scale discrete components into micrometer or nanometer-scaled embedded thin-film components. In the SoP domain, LTCC technology offers many attractive features and possibilities such as the arbitrary number of layers, which not only allows embedded passives but also facilitates their vertical integration with other RF components. Unlike the on-chip antenna integration efforts [6-7], the low loss nature of the LTCC substrates at microwave and millimeter-wave frequencies makes them very suitable for efficient antenna design [8]. However, not much work has been done so far on the LTCC based automotive radars. In [9], an LTCC based automotive radar system at 24 GHz has been demonstrated. However, it employs an eight-patch antenna array with a complex feed network, which renders the system to be bulky and inefficient. Moreover the design in [9] does not cater for harsh automotive environments.

Our previous work has demonstrated that the aperture-coupled technique is very suitable for LTCC medium [10]. We have also demonstrated that antenna gain can be enhanced manifolds utilizing a magnetic superstrate in LTCC SoP design [11]. This paper presents a 24 GHz LTCC automotive radar SoP comprising of an integrated array of aperture coupled patch antennas and a thin high dielectric constant superstrate layer. For the first time, the gain resonance method [12] has been incorporated in LTCC, which is perfectly suited to the multilayer nature of this medium. This superstrate layer not only enhances the array gain but also adds to the robustness of the design by providing protection against severe conditions. Unlike [9], the design is simple, efficient and cost effective yet provides almost similar performance. The miniaturized module can easily fit into car bumpers or side mirrors etc. The paper highlights the design steps with some key results and also discusses some important design tradeoffs.

II. CONCEPT

The goal of this work is to integrate the RF circuits, embedded passives including antenna, and interconnects into a

single compact package utilizing advanced LTCC technology. The concept of this SoP is shown in Figure 1. It employs a unique combination of two different dielectric constant LTCC layers. This combination allows the first practical implementation of the gain resonance method in LTCC medium. The substrate is realized in a relatively low dielectric constant (ϵ_{r1}) LTCC material CT 707 and the superstrate makes use of a high dielectric constant (ϵ_{r2}) LTCC material. The material properties of both types of LTCC are listed in Table 1. Each layer's fired thickness is 100 μm . The bottom substrate (SUBS1) is five layers thick with a four layer thick cavity to accommodate the Microwave and Millimeter-Wave Integrated circuit (MMIC). The MMIC is a flip-chip, which rests on the solder balls in the cavity. The output of the MMIC is connected to the feed microstrip line on a single layer thick middle substrate (SUBS2) through a solder ball and via combination. There is a two layer thick middle substrate (SUBS3) in between the feed microstrip and the ground plane. The ground plane contains an aperture, which is excited by the feed microstrip line. The top substrate (SUBS 4) is five layers thick and contains the patch antenna array. Finally, a four layers thick superstrate is placed above SUBS4 with an air gap of 0.4 mm between them to enhance the gain of the antenna array and improve the robustness of the module. The air gap is realized through four posts of CT 707 material.

Table 1 - Properties of LTCC Materials

Properties	LTCC Materials	
	CT707	CT767
Relative Dielectric Constant	6.39	68.7
Loss Tangent	0.00481	0.00173
Thickness of each layer (μm)	100	100

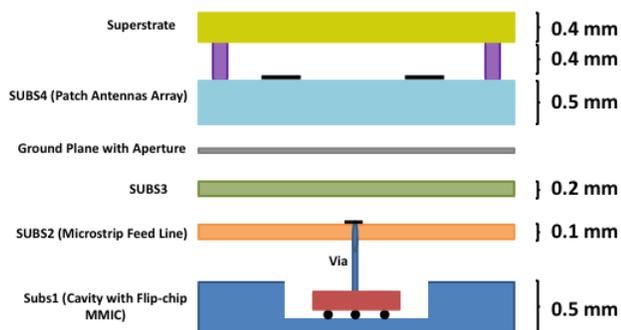


Figure 1 – SoP concept

III. ANTENNA ARRAY DESIGN

The aperture coupled patch topology utilizes a common ground plane between the radiating antenna and the feed line. For this work, aperture coupling is employed mainly because the ground plane in between the patch antenna array and the MMIC acts as a shield for the circuits. A limitation of this technique is that the aperture in the ground plane can radiate considerably in the backward direction. However, by choosing

the right slot length with respect to the patch size can minimize this unwanted radiation.

Array designs are employed in order to increase the gain achieved from a single antenna element. More the number of antenna elements in the array, higher is the overall gain. However, tradeoffs are the added complexity of the feed network, which enhances the substrate losses and the larger size of the module because of the additional antenna elements. In this work, the array comprises only two aperture coupled patch antennas fed by a single microstrip split into two lines. The complete SoP has been designed in an EM simulator HFSSTM.

At first a single patch antenna element is designed for the required center frequency of 24 GHz. The antenna is fed through the aperture in the ground plane, which in turn is fed through a microstrip line. The microstrip feed line is excited through a lumped port in HFSSTM. The width of the feed line is 1 mm, which corresponds to a characteristics impedance of 50 Ω . The length of the slot plays a vital role in determining the resonant frequency of the antenna and also helps in optimizing the input impedance of the antenna design. On the other hand, the slot width is critical in controlling the backward radiation from the antenna. The slot length and width are optimized to be 2.3 mm and 0.1 mm respectively. This helps in achieving the desired radiation pattern with minimum backward radiation as shown in Figure 2 (a). The length of the patch antenna is optimized to be 2.13 mm while its width is 2.4 mm. A good match with an S11 of -16 dB and a gain of 4.6 dB is attained at 24 GHz.

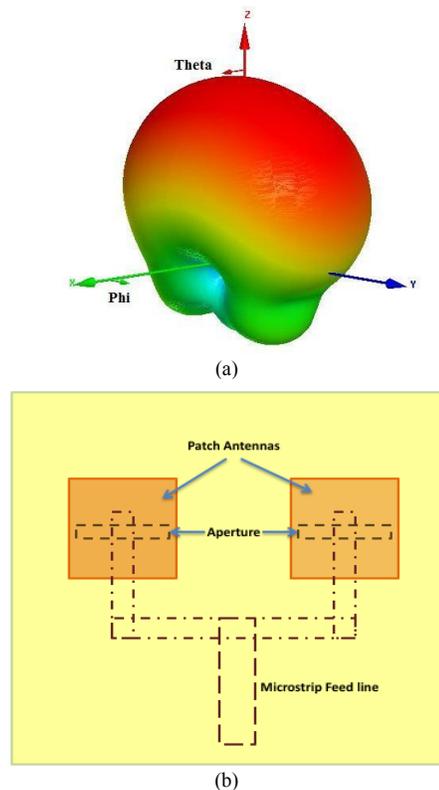


Figure 2 – Antenna Array (a) Design (b) Radiation Pattern of Single Element Patch

As shown in Figure 2 (b), each element in the antenna array is fed through the a separate aperture in the ground plane, which in turn are excited through the microstrip lines divided in a T-fashion from the main microstrip feed line. The widths of these divided microstrip lines are 0.5 mm each and correspond to an impedance of 100 Ω. The two 100 Ω lines connected in parallel match perfectly to the 50 Ω main feed line. The antenna elements in the array have identical dimensions. The separation of 6.25 mm between the two patch elements corresponds to half free space wavelength ($0.5\lambda_o$). The reflection coefficient of the single element and the complete array are shown in Figure 3. A good impedance match at 24 GHz is observed in both the cases. The gain of the array is 5.8 dB as compared to 4.6 dB of the single element. The radiation pattern of the array, as shown in Figure 4, has narrowed a little from the broad bore-sight lobe in case of single element, which is expected due to increased gain. Moreover, the coupling between the two patch elements has resulted in enhanced back lobe levels. However, these can be reduced, if required, by further optimizing the aperture dimensions. Similarly, due to the thick LTCC substrate the gain of the two-patch LTCC array is slightly lower as the power is lost in surface waves. Higher gain can be achieved by replacing the thick substrate with thin LTCC layers having lower dielectric constant (close to air) as the antenna substrate.

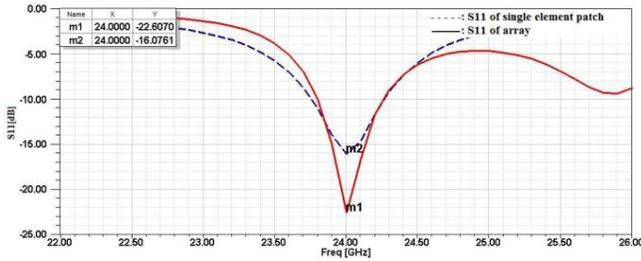


Figure 3 - Reflection Coefficient S11 of Array

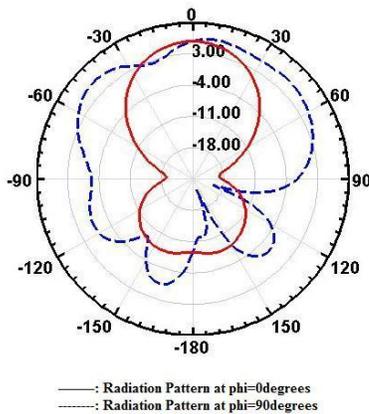


Figure 4 - Radiation Pattern of Aperture Coupled Array

IV. GAIN ENHANCEMENT TECHNIQUE

The gain enhancement technique employed here is ideally suited to LTCC environment, as it requires the addition of a superstrate layer over the substrate. This is referred to as the resonance gain method and utilizes a superstrate with either relative permittivity, $\epsilon_r > 1$ or relative permeability, $\mu_r > 1$. By choosing the layer thicknesses and antenna position properly, a very large gain may be realized at any desired angle. The gain varies proportionally to either ϵ_r or μ_r , depending on the configuration. However, the bandwidth is seen to vary inversely to gain so that a reasonable gain limit is actually established for practical antenna operation [11], [12]. The gain resonant condition for employing a $\epsilon_r > 1$ superstrate is given as [12]:

$$\frac{\eta_2 t}{\lambda_o} = \frac{2p-1}{4} \quad (1)$$

$$\frac{\eta_1 z_o}{\lambda_o} = \frac{2n-1}{4} \quad (2)$$

$$\frac{\eta_1 B}{\lambda_o} = \frac{m}{2} \quad (3)$$

Where η_1 and η_2 are the refractive indices of the substrate and superstrate respectively. B and t represent the thicknesses of the substrate and superstrate respectively. Here z_o is the height of the antenna with respect to the bottom of the substrate, p, m and n are positive integers. For this design, the ϵ_{r1} of 6.39 and the ϵ_{r2} of 68.7 give η_1 of 2.52 and η_2 of 8.28 respectively. Choosing p = 1 in (1) and calculating for 24 GHz, the required t = 0.377 mm or 4 layers. Similarly, selecting n and m equal to 1, (2) and (3) give B = 2.47 mm or 25 layers, and $z_o = 1.23$ mm or 13 layers. Since 24 layers for substrate is not a cost effective solution, therefore the substrate thickness is restricted to the height required for locating the antenna, i.e. 1.3 mm or 13 layers. An air box is also included in between the substrate and superstrate like [11] and its thickness is estimated through parametric simulations in HFSS to determine the highest gain. The results are shown in Figure 5.

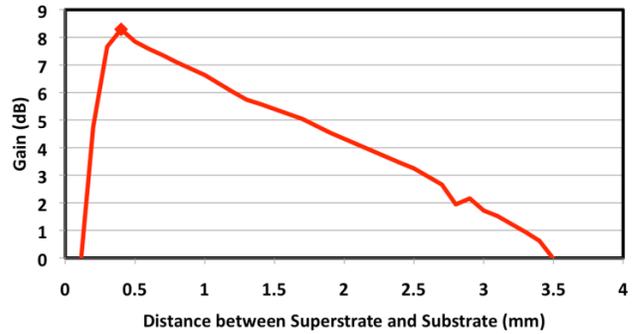


Figure 5- Gain vs. Distance between Superstrate and Substrate

As can be observed from Figure 5, the superstrate provides a gain enhancement of more than 2 dB at a height of

0.4 mm above the patch array. The final gain with the array and superstrate combination is 8.3 dB. Besides the increase in the gain, the radiation pattern of the array is also affected by the superstrate loading and now has higher side lobe levels. This means that the spacing between the elements has to be re-adjusted after loading of the superstrate. Though the impedance of the antenna array is not much influenced by the superstrate loading, however a downward shift is observed in the 10 dB bandwidth, as shown in Figure 6. It is worth mentioning here that the gain of the array and superstrate combination can be increased manifolds, provided the substrate thickness is increased. However, the gain achieved with the present arrangement is enough for the medium range automotive radar applications. This can be further explained with the help of the following example.

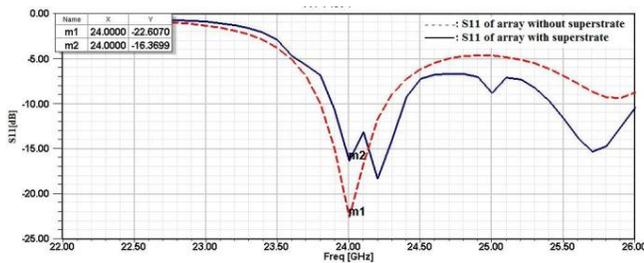


Figure 2 - Comparison of S11 of array with superstrate and without superstrate

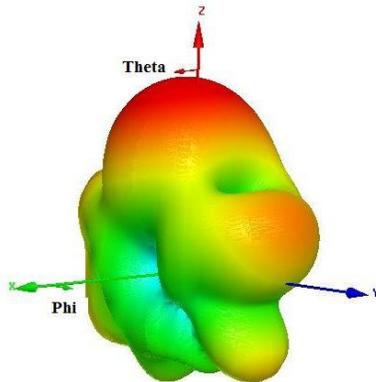


Figure 3 - Radiation Pattern of array with the superstrate

By employing the Friis transmission equation [13] the range of the SoP module can be determined. In a typical automotive radar application, the transmitting power (P_t) from the transceiver is 10 dBm at the antenna port [14]. The standard sensitivity of a typical receiver is around -100 dBm. Keeping into account the fading effect margin of 30 dB, let us assume the received power (P_r) is -70 dBm. Let us also consider that transmit and receive antennas are identical so the antenna gain for transmit (G_t) and receive (G_r) sides is 8.3 dB. The resonant frequency is 24 GHz, which corresponds to a free space wavelength λ_o of 12.5 mm. By substituting all these values in Friis equation, the range can be calculated as follows.

$$P_r = P_t \times G_t \times G_r \times \left(\frac{\lambda_o}{4\pi R} \right)^2 \quad (4)$$

A communication range of 67 m is achieved, which is quite suitable for the medium range automotive radar applications. The range can be enhanced, if required, by further increase in antenna gain or transmitted power.

V. CONCLUSION

A unique design for an aperture coupled patch antenna array SoP realized in a mixed LTCC medium of low and high dielectric constants is presented. The substrate, realized in relatively low dielectric constant material, has a custom cavity to accommodate the MMIC. Shielding is provided by a ground plane between the circuits inside the cavity and the antennas on the top substrate layers. A superstrate in high dielectric medium not only enhances the gain of the module but also acts as an additional protection layer for harsh automotive environments. Analysis has revealed that a suitable communication range can be obtained through this design with standard transmission power levels for UMRR application.

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